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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/776,381	02/11/2004	Farbod Aram	MP0478	3909
23624	7590	06/27/2005	EXAMINER	
MARVELL SEMICONDUCTOR, INC. INTELLECTUAL PROPERTY DEPARTMENT 700 FIRST AVENUE, MS# 509 SUNNYVALE, CA 94089				NGUYEN, KHAI M
		ART UNIT		PAPER NUMBER
		2819		

DATE MAILED: 06/27/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

JC

Office Action Summary	Application No.	Applicant(s)	
	10/776,381	ARAM, FARBOD	
	Examiner	Art Unit	
	Khai M. Nguyen	2819	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 11 February 2004.
 2a) This action is FINAL. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-29 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) 24-29 is/are allowed.
 6) Claim(s) 1-3,13-15 and 20-23 is/are rejected.
 7) Claim(s) 4-12 and 16-19 is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on 2/11/2004 is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ . |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ . | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| | 6) <input type="checkbox"/> Other: _____ . |

DETAILED ACTION

Specification

1. The application has not been checked to the extent necessary to determine the presence of all possible typographical and grammatical errors. However, Applicant's cooperation is requested in correcting any errors of which he/she may become aware in the application.

Claim Rejections - 35 USC § 112

2. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claim 1 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Claim 1 recites, "said broadband amplifier" which lacks antecedent basis. Correction/clarification is required.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-3, 13-15, and 20-23 are rejected under 35 U.S.C. 102(e) as being anticipated by Marz (US 6,731,165 B1).

Regarding claims 1-2, Marz discloses a compensation circuit for an amplifier (Fig. 2) including at least first and second amplifier stages (206/218), comprising: a first capacitance component (224; column 4, lines 12-15) having one end (226) that communicates with an input (208) of the first amplifier stage; an amplifier (broadband amplifier 236) having a gain, an input (238) that communicates with an opposite end (228) of the first capacitance component (224), and an output (242); and a second capacitance component (212; column 4, lines 12-15) having a first end (216) that communicates with the output (242) of the amplifier (236) and an opposite end (214) that communicates with an input (220) of the second amplifier stage (218).

Regarding claim 3, Marz discloses the apparatus of the above claims including a current source means (230); and a transimpedance amplifier (236) having an input (238) that communicates with the current source means, and a voltage output (V2) at an output terminal (248).

Regarding claims 13-14, & 20, Marz discloses a circuit (Fig. 2), comprising: a first amplifier stage (206) having an input (208) and an output (210); a second amplifier stage (218) having an input (220) that communicates with the output of the first amplifier stage and an output (204) for driving a load; and a compensation circuit for an amplifier (Fig. 2) including: a first capacitance component (224; column 4, lines 12-15) having one end (226) that communicates with the input (208) of the first amplifier stage; an amplifier (broadband amplifier 236) having a gain, an input (238) that communicates

with an opposite end (228) of the first capacitance component (224), and an output (242); and a second capacitance component (212; column 4, lines 12-15) having a first end (216) that communicates with the output (242) of the amplifier (236) and an opposite end (214) that communicates with an input (220) of the second amplifier stage (218).

Regarding claim 15, Marz discloses the apparatus of the above claims including a current source means (230); and a transimpedance amplifier (236) having an input (238) that communicates with the current source means, and a voltage output (V2) at an output terminal (248).

Regarding claims 21-23, Marz discloses (Figs. 2 & 4; column 4, lines 12-15) the apparatus of above claims including a first impedance (484, Fig. 4) that has one end that communicates with the input of the first stage amplifier stage (206, Fig. 2); and a second impedance (230, Fig. 2) that has one end (232) that communicates with the input (via 224) of the first amplifier stage (206) and an opposite end (232) that communicates with the output of the second amplifier stage.

Prior Art

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclose (see recited references on USPTO-892 Form attached).

Allowable Subject Matter

4. Claims 4-12, and 16-19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. Claims 24-29 allowed.

Contact Information

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khai M. Nguyen whose telephone number is 571-272-1809. The examiner can normally be reached on 9:00 - 5:30 Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert (Bob) J. Pascal can be reached on 571-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

KN
June 10, 2005


PEGUY JEANPIERRE
PRIMARY EXAMINER